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## ABSTRACT OF THE DISCLOSURE

An information processor includes a CPU that calculates various kinds of data, a clock circuit that measures time independently of the CPU, an operational condition switching circuit that makes the CPU switch between sleep and active conditions according to predetermined conditions, and a clock adjustment function that is executed by the CPU, so as to output time by adjusting the time measured by the clock circuit, at a predetermined time. The information processor also includes a condition inputting circuit that inputs a recovery condition, to switch the CPU from the sleep condition to the active condition, when the time measured by the clock circuit comes to a clock adjustment time when the clock adjustment function is executed by the CPU, or a predetermined time before the clock adjustment time. The adjustment function can be properly operated when a sleep function for a control system is used in combination with the adjustment function that operates independently of the control system. The information processor may be provided to, for example, a facsimile apparatus or a telephone.